

FIG. 1 is a block diagram of a channel controller system.

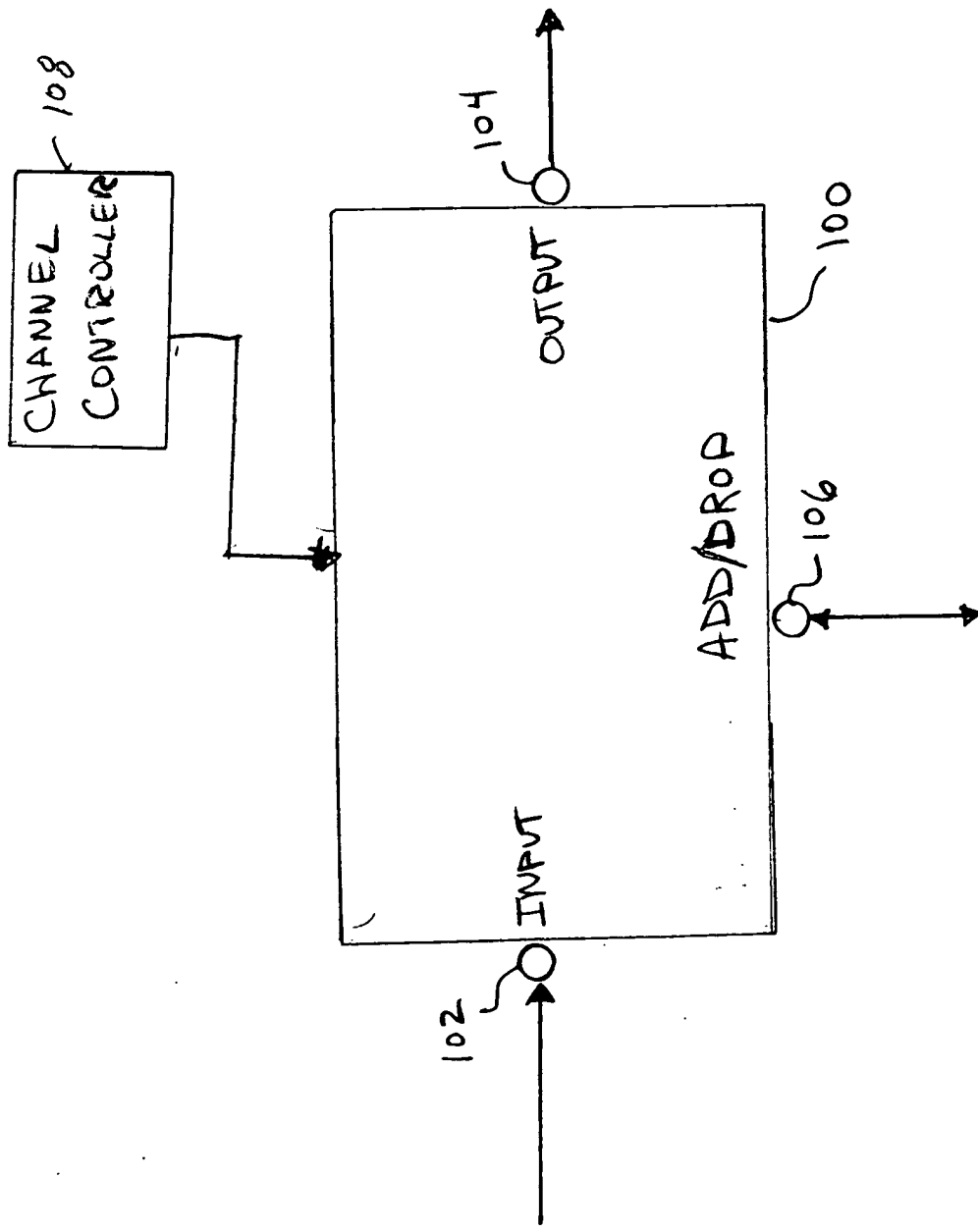
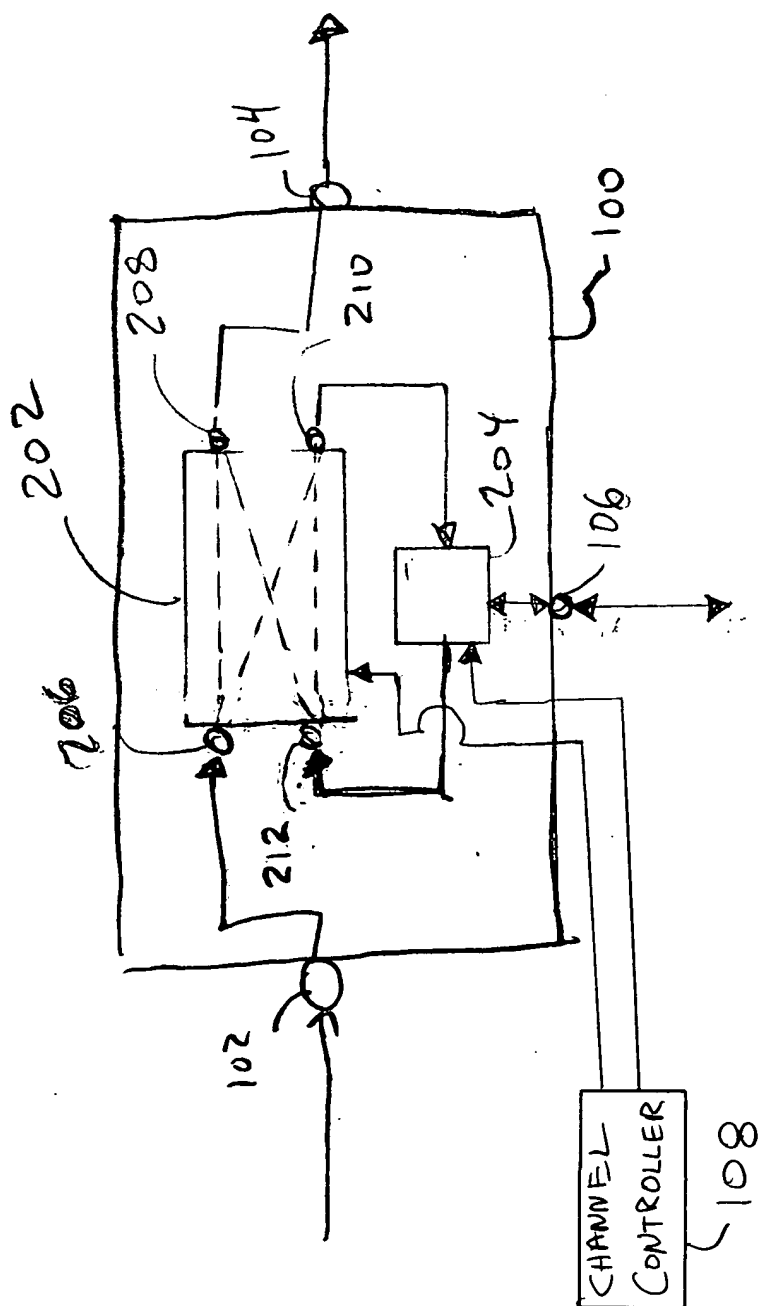


FIG. 1

$\{u_{\alpha\beta}^{(m)}\}$ and $\{u_{\alpha\beta}^{(n)}\}$ are the m th and n th order terms of the asymptotic expansion of $u_{\alpha\beta}$ in powers of ϵ . The functions $u_{\alpha\beta}^{(m)}$ and $u_{\alpha\beta}^{(n)}$ are determined by the boundary conditions and the equations of motion. The functions $u_{\alpha\beta}^{(m)}$ and $u_{\alpha\beta}^{(n)}$ are determined by the boundary conditions and the equations of motion.



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FIG. 3 is a schematic diagram of a channel controller system.

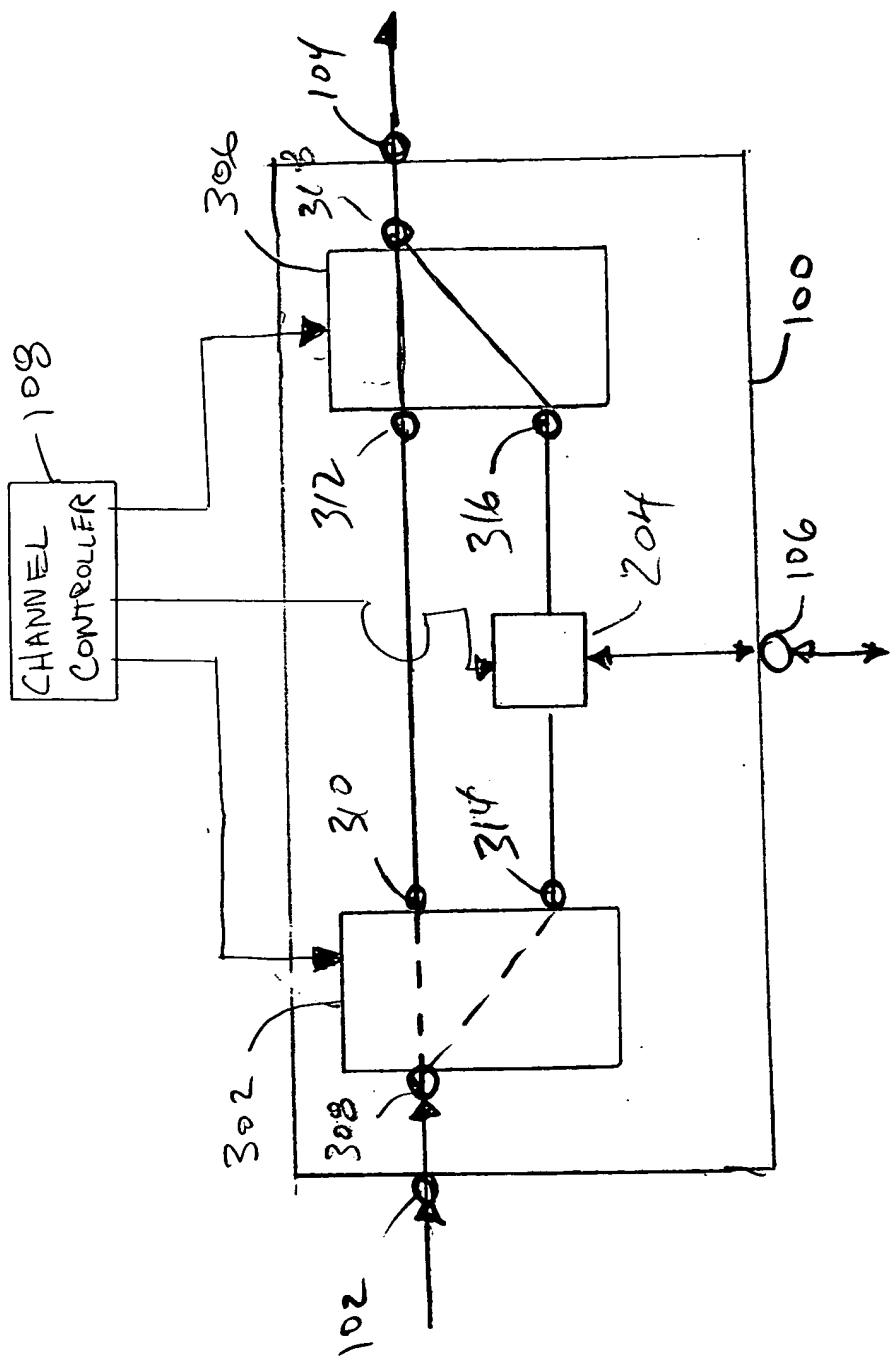


FIG 3

FIG. 4 is a block diagram of a channel controller system in accordance with the present invention.

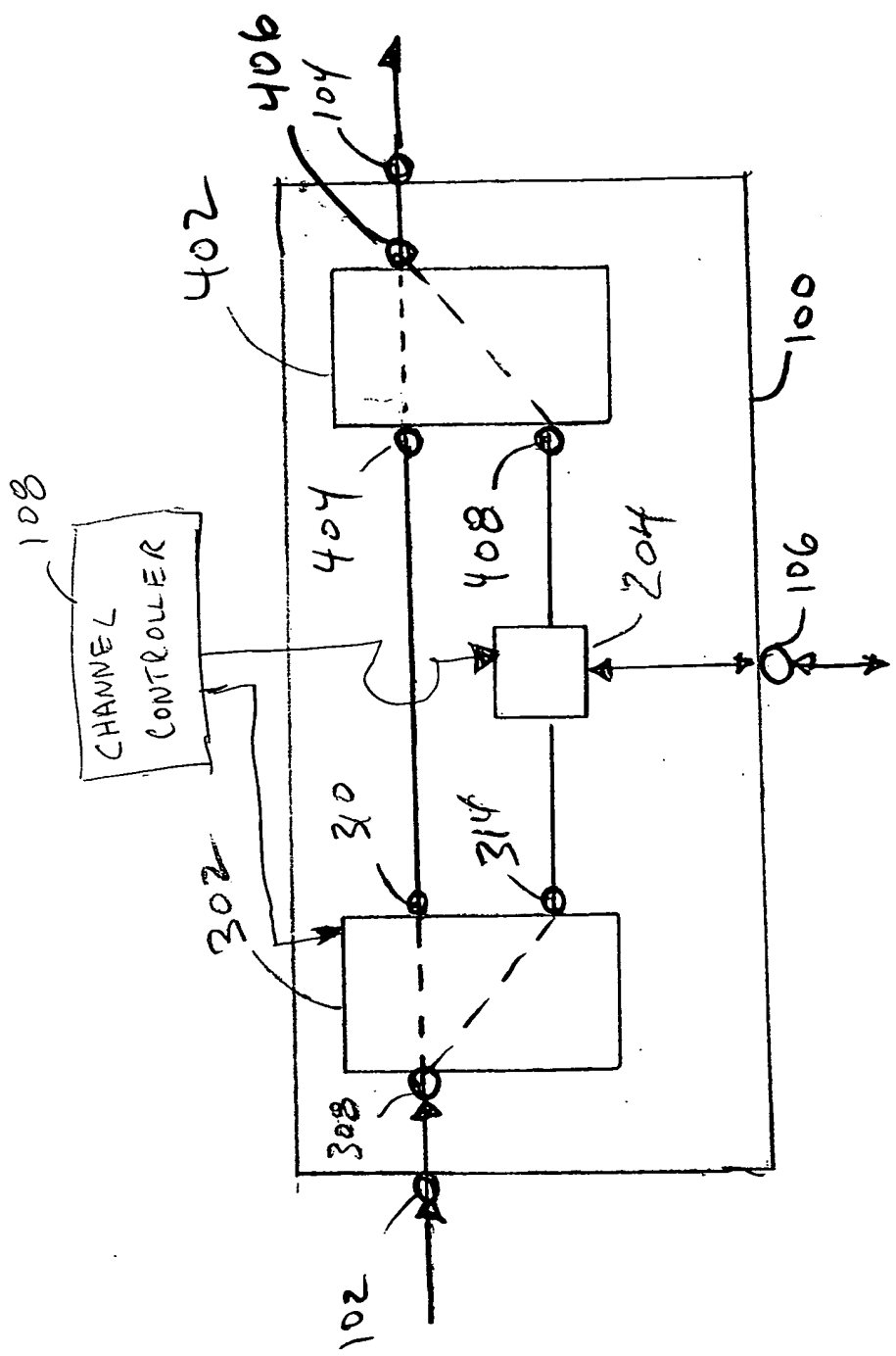


FIG. 4

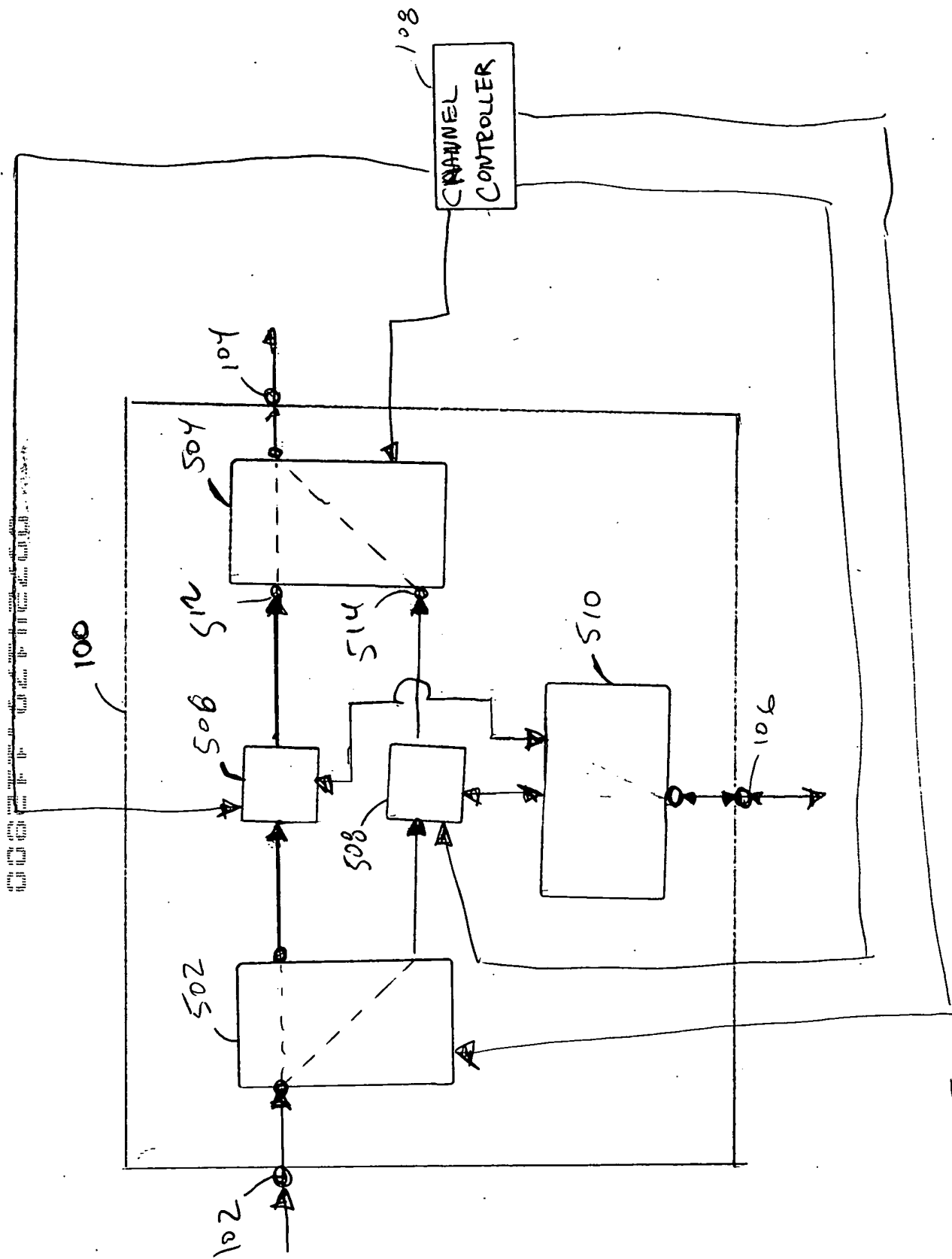


FIG 5

FIG. 6A is a block diagram of a system 100 for controlling a channel controller 108. The system 100 includes a plurality of input devices 602, 604, 606, 608, 610, 612, 614, 616, 618, 620, 622, 624, 626, 628, 630, 632, 634, 636, 638, 640, 642, 644, 646, 648, 650, 652, 654, 656, 658, 660, 662, 664, 666, 668, 670, 672, 674, 676, 678, 680, 682, 684, 686, 688, 690, 692, 694, 696, 698, 700, 702, 704, 706, 708, 710, 712, 714, 716, 718, 720, 722, 724, 726, 728, 730, 732, 734, 736, 738, 740, 742, 744, 746, 748, 750, 752, 754, 756, 758, 760, 762, 764, 766, 768, 770, 772, 774, 776, 778, 780, 782, 784, 786, 788, 790, 792, 794, 796, 798, 800, 802, 804, 806, 808, 810, 812, 814, 816, 818, 820, 822, 824, 826, 828, 830, 832, 834, 836, 838, 840, 842, 844, 846, 848, 850, 852, 854, 856, 858, 860, 862, 864, 866, 868, 870, 872, 874, 876, 878, 880, 882, 884, 886, 888, 890, 892, 894, 896, 898, 900, 902, 904, 906, 908, 910, 912, 914, 916, 918, 920, 922, 924, 926, 928, 930, 932, 934, 936, 938, 940, 942, 944, 946, 948, 950, 952, 954, 956, 958, 960, 962, 964, 966, 968, 970, 972, 974, 976, 978, 980, 982, 984, 986, 988, 990, 992, 994, 996, 998, 1000.

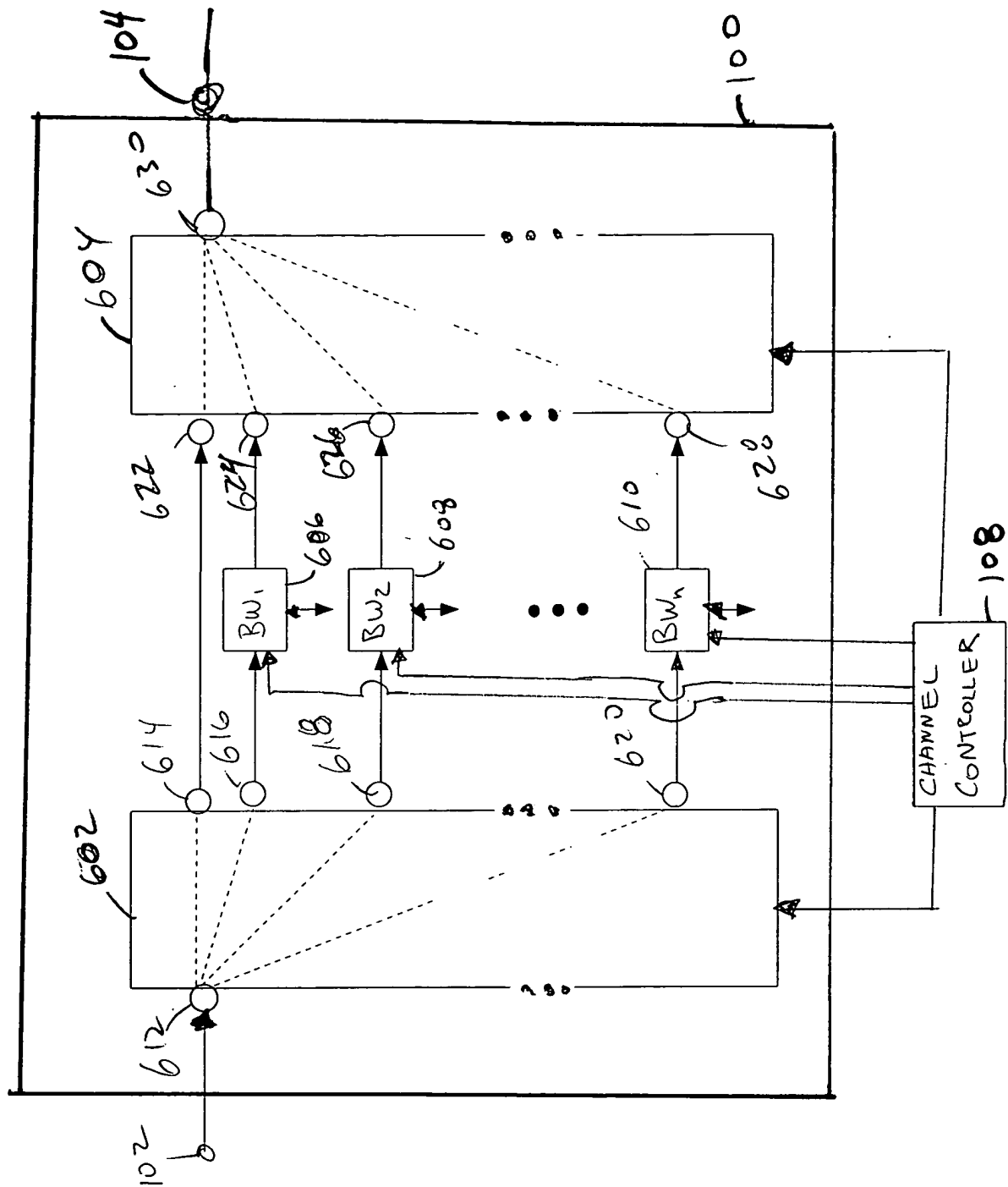


FIG. 6A

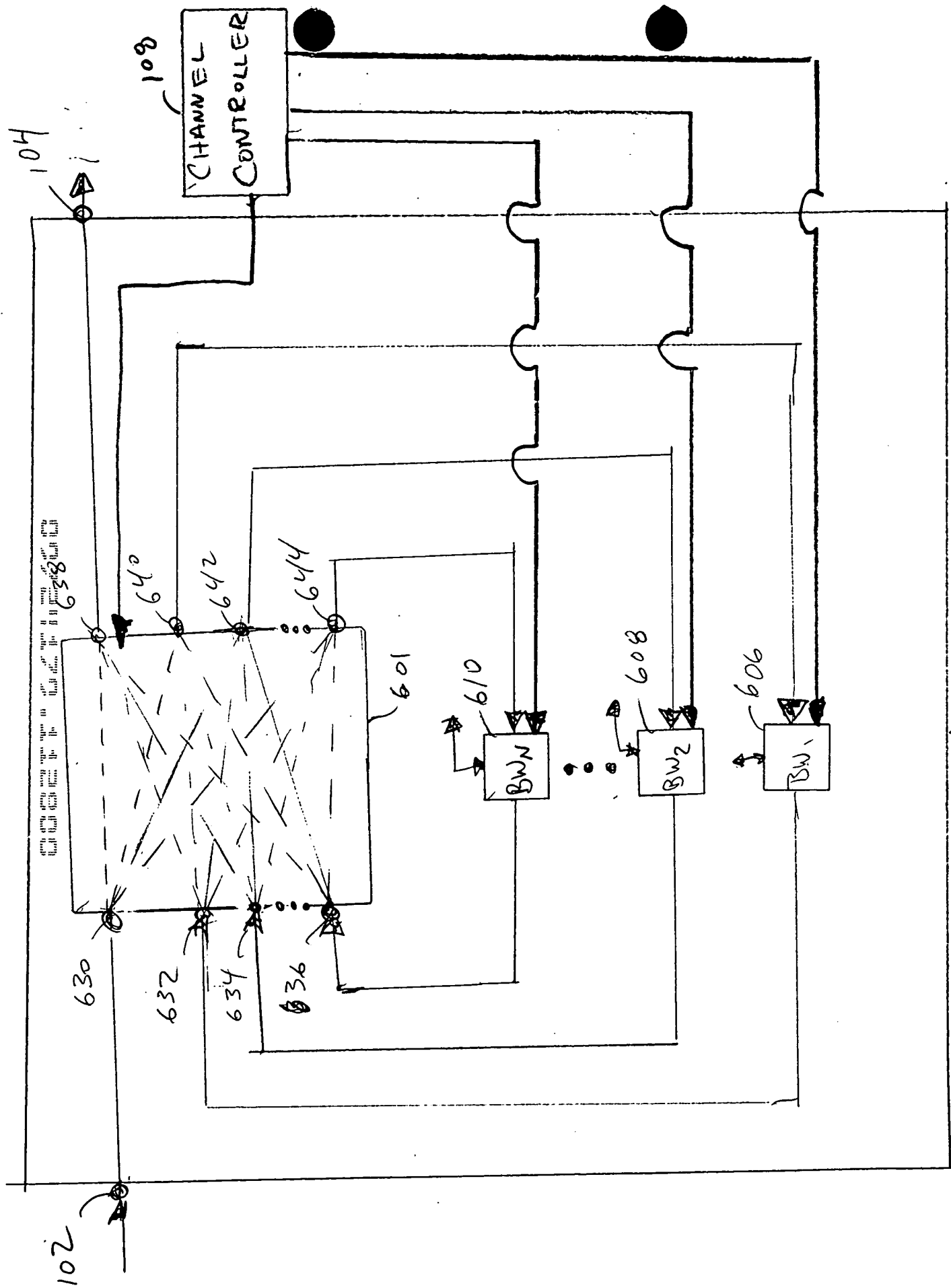


FIG. 6B

100

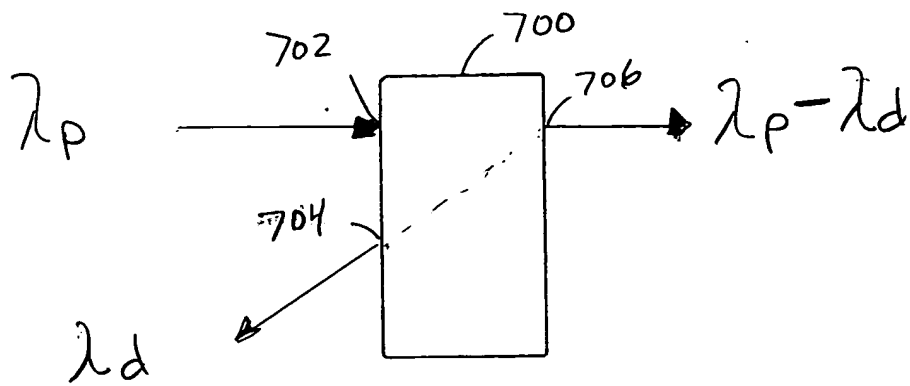


FIG. 7

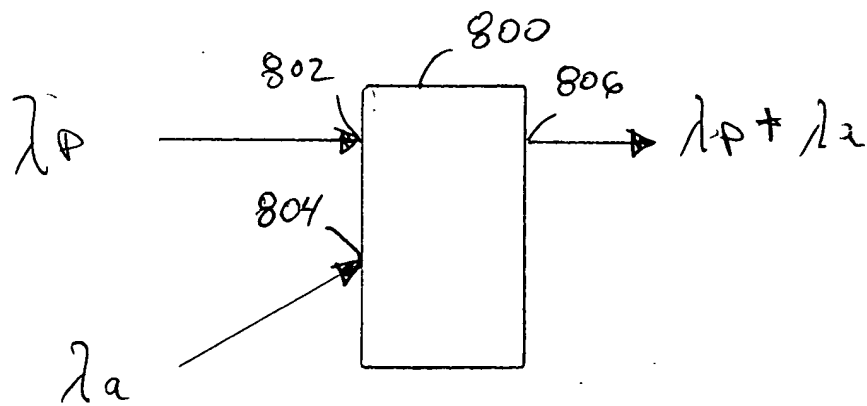


FIG. 8

FIG. 9 is a block diagram of a system 700 for processing a sequence of inputs $\lambda_1, \lambda_2, \dots, \lambda_n$ to produce an output $\lambda_p - \sum_{k=0}^n \lambda_k$. The system 700 includes a series of blocks 700, each receiving an input λ_k and producing an output $\lambda_p - \lambda_k$. The outputs of the blocks 700 are summed to produce the final output $\lambda_p - \sum_{k=0}^n \lambda_k$.

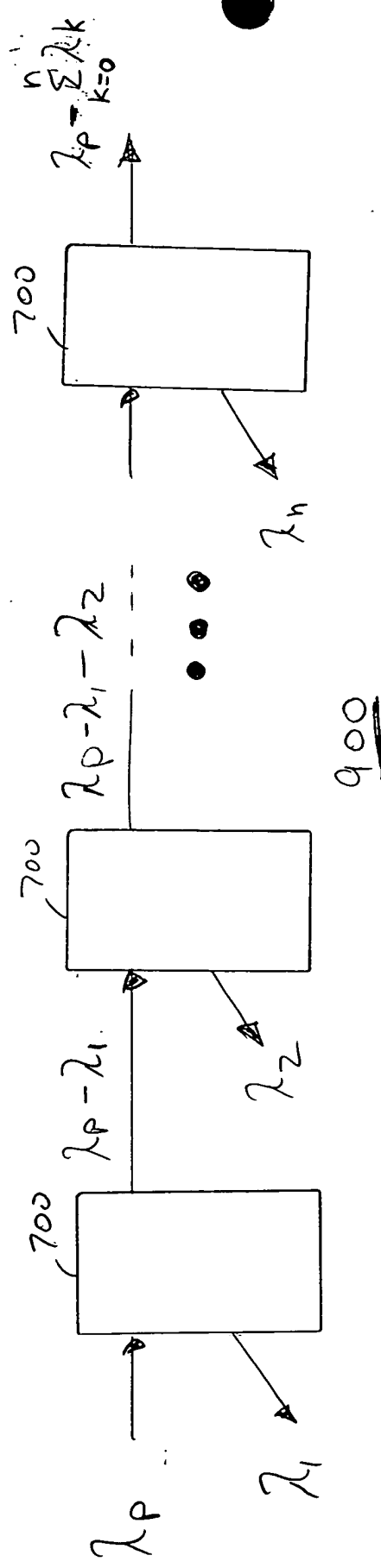


FIG. 9

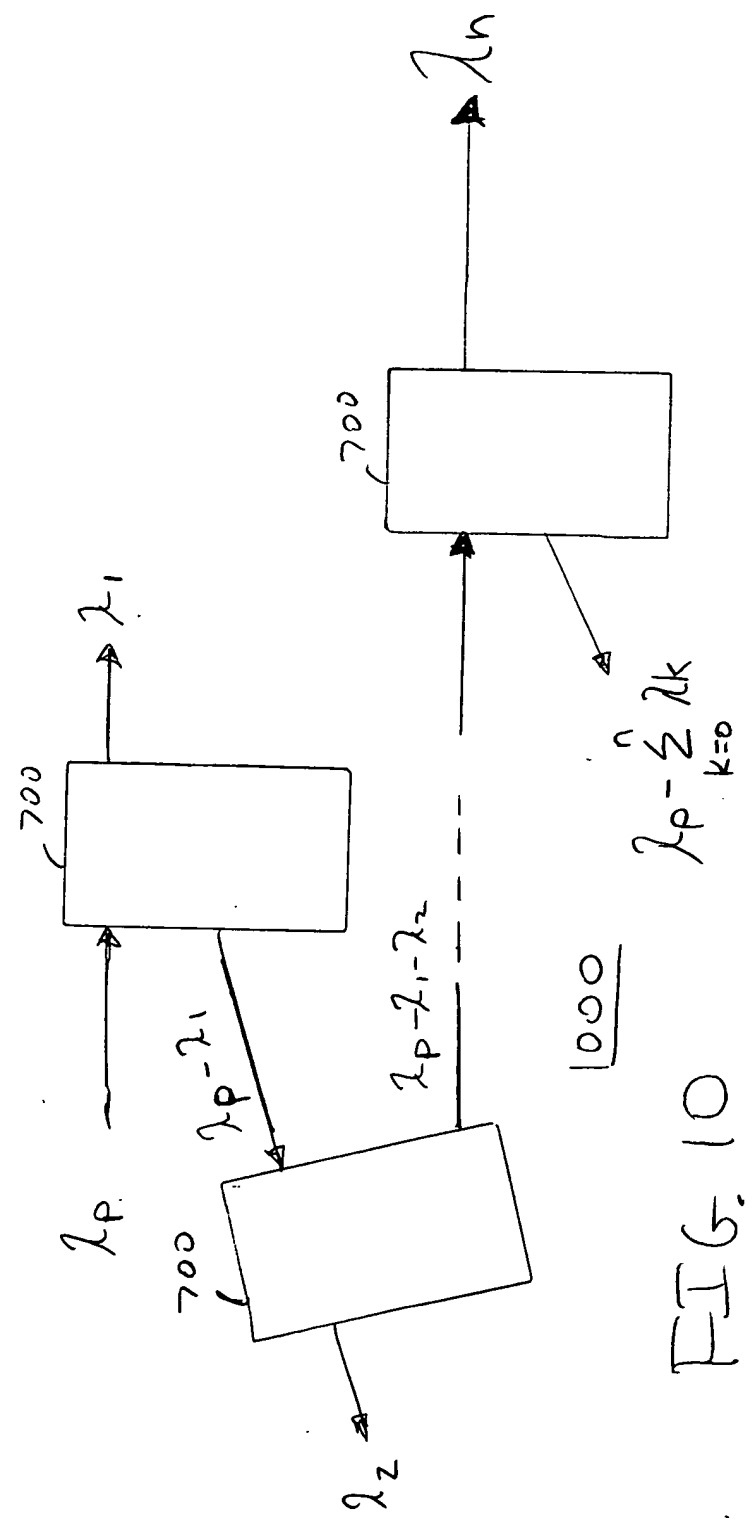


FIG. 10

FIG. 11

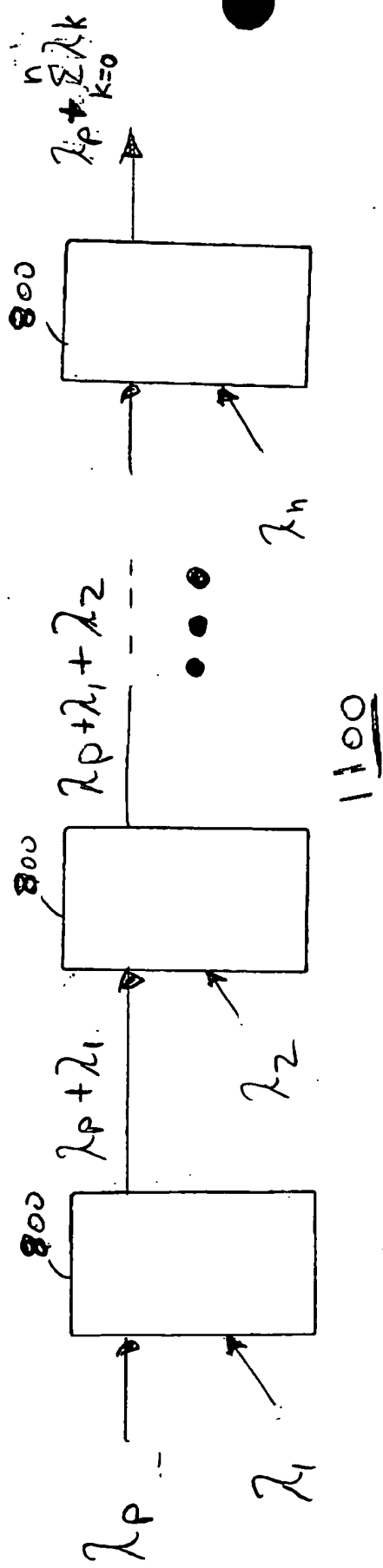


FIG. 11

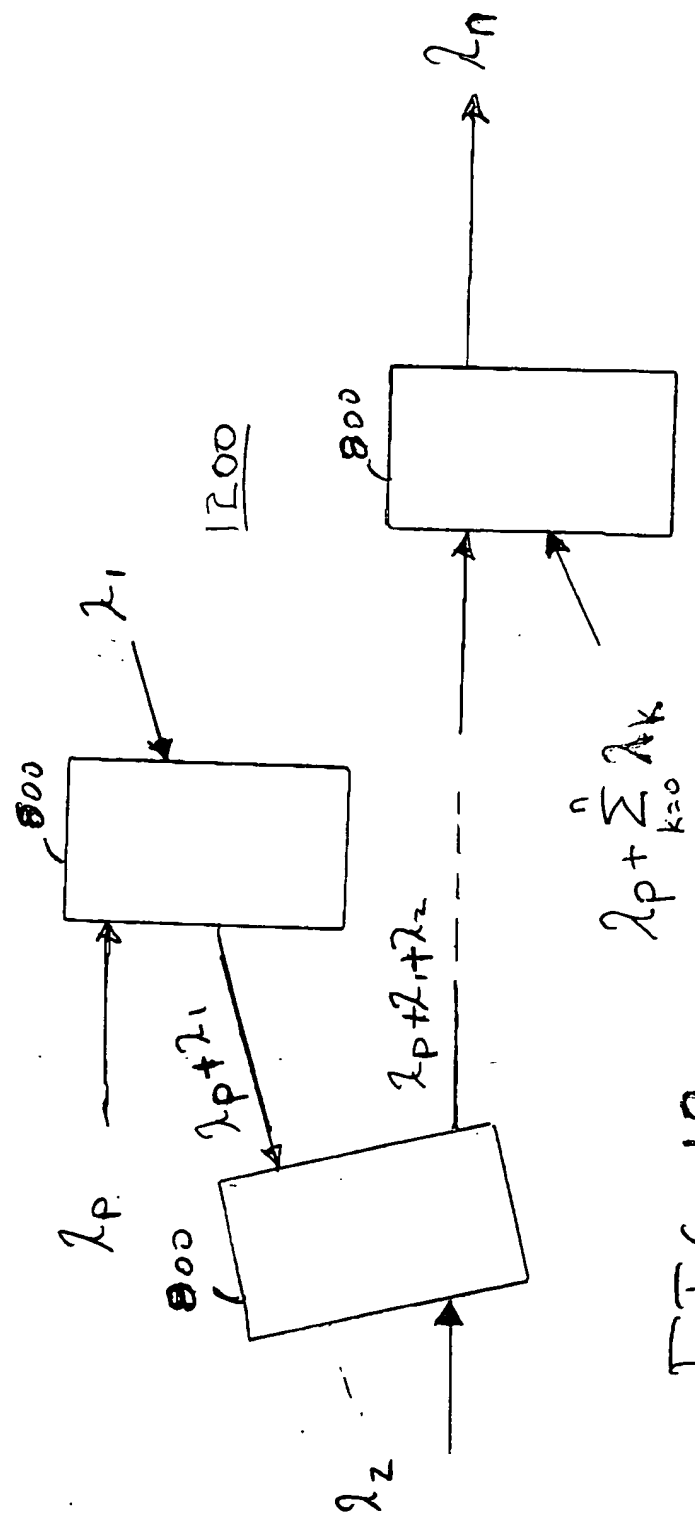


FIG. 12

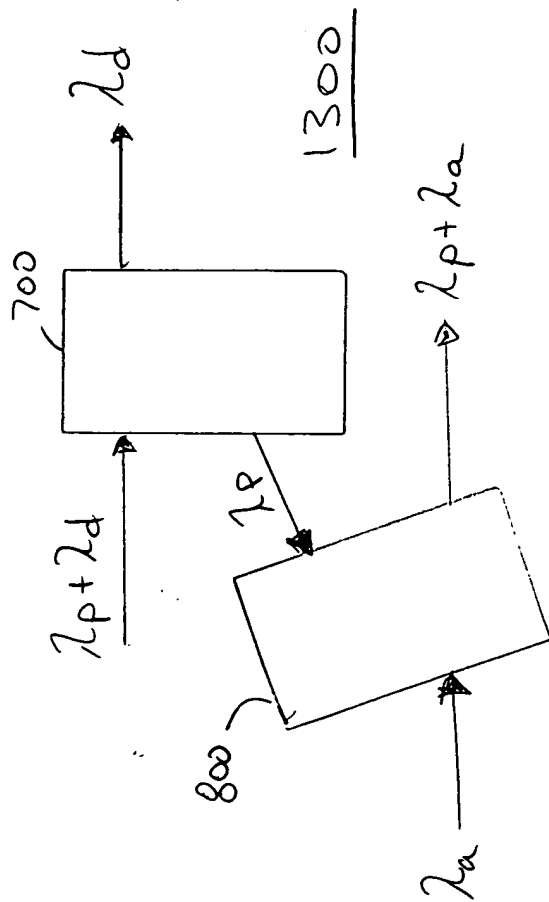


FIG. 13

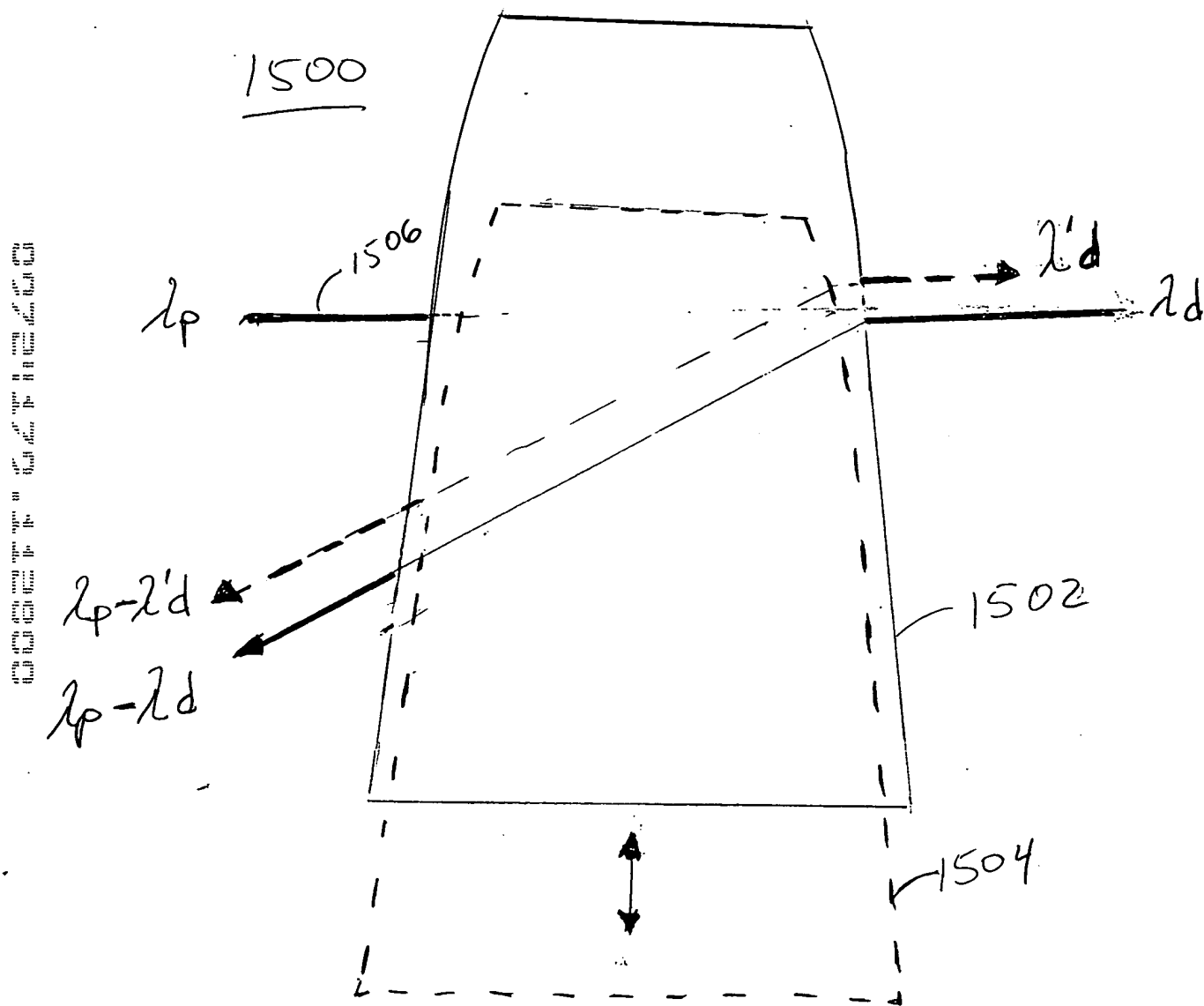


FIG. 15

FIG. 16

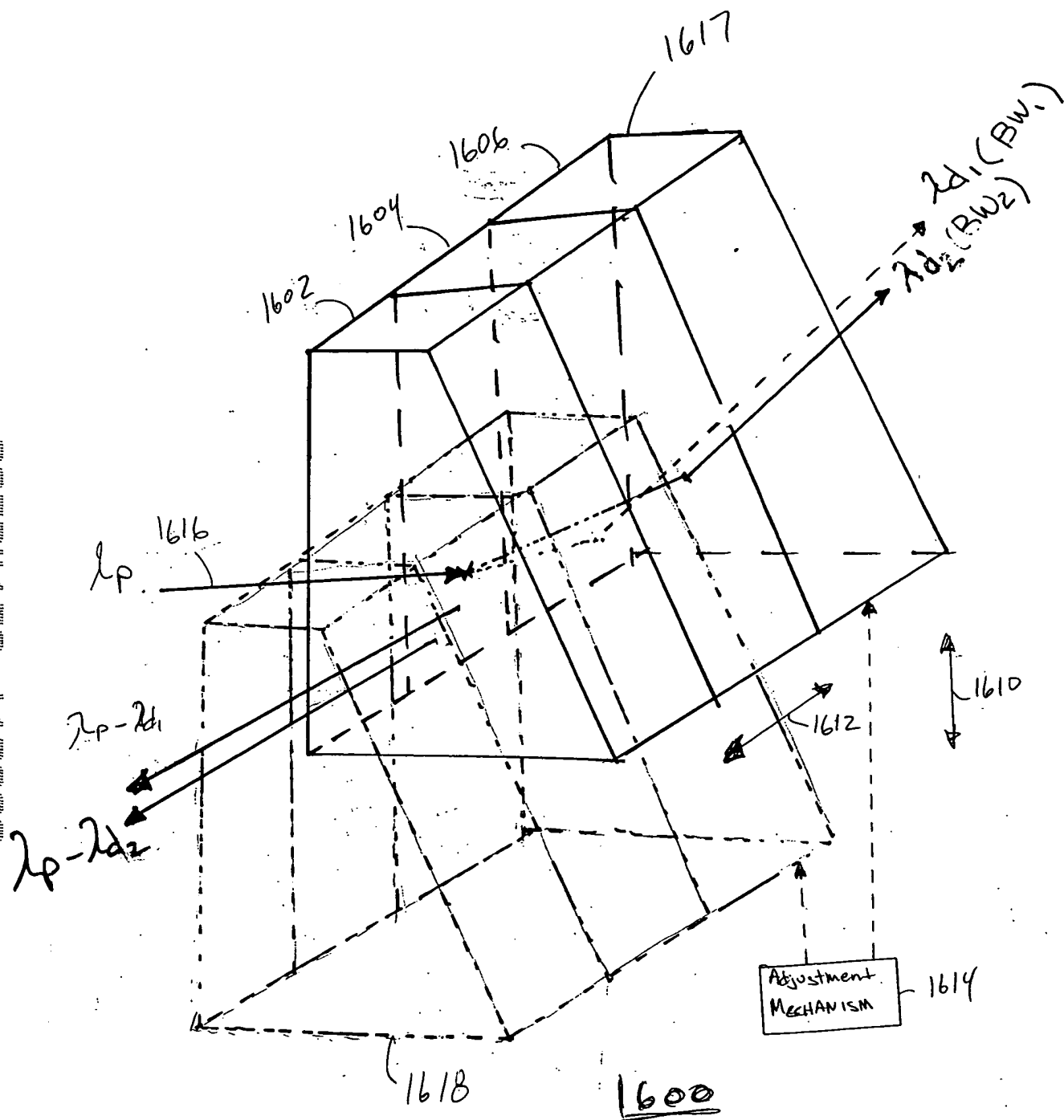
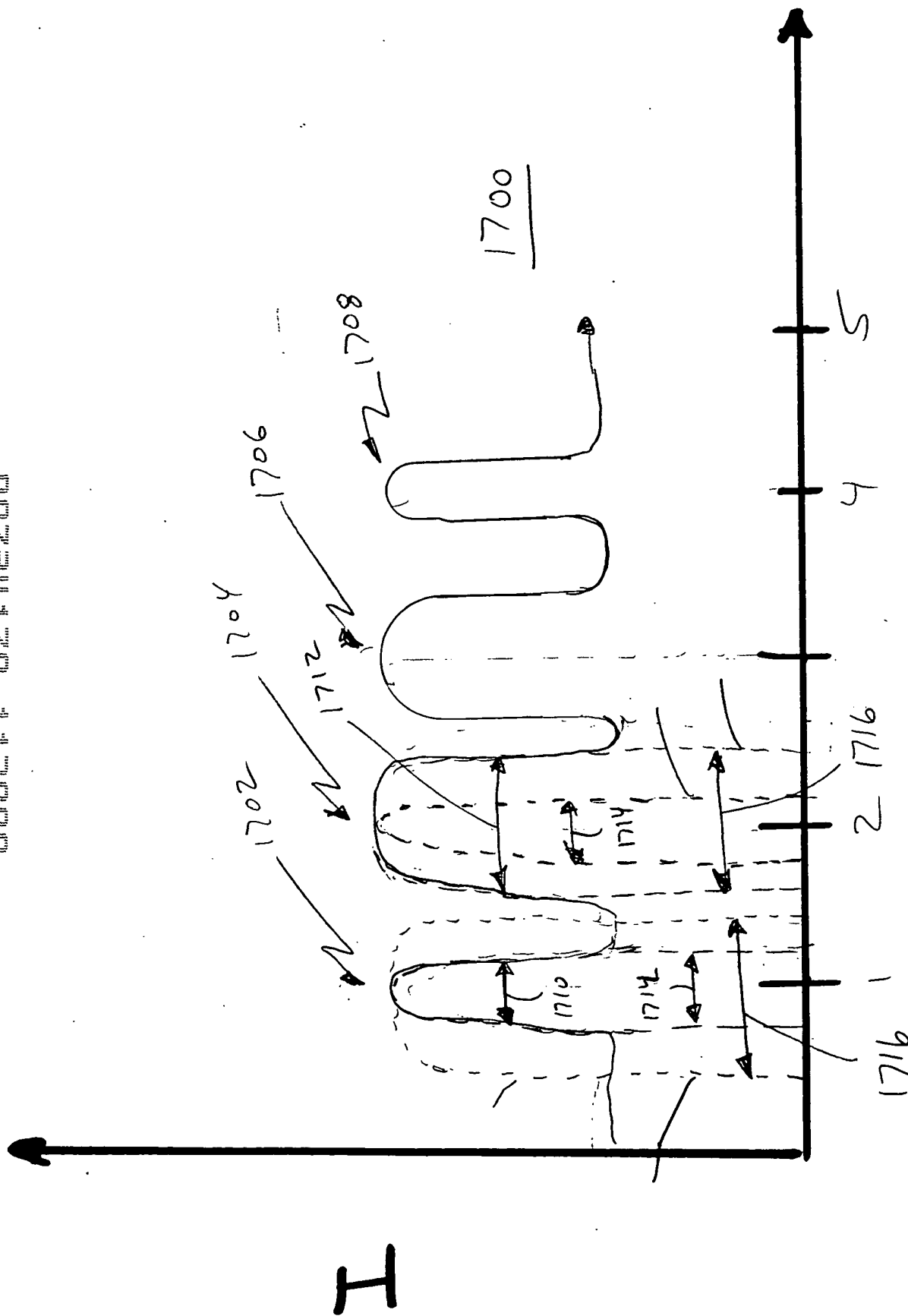


FIG. 16

FIG. 17



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FIG. 17

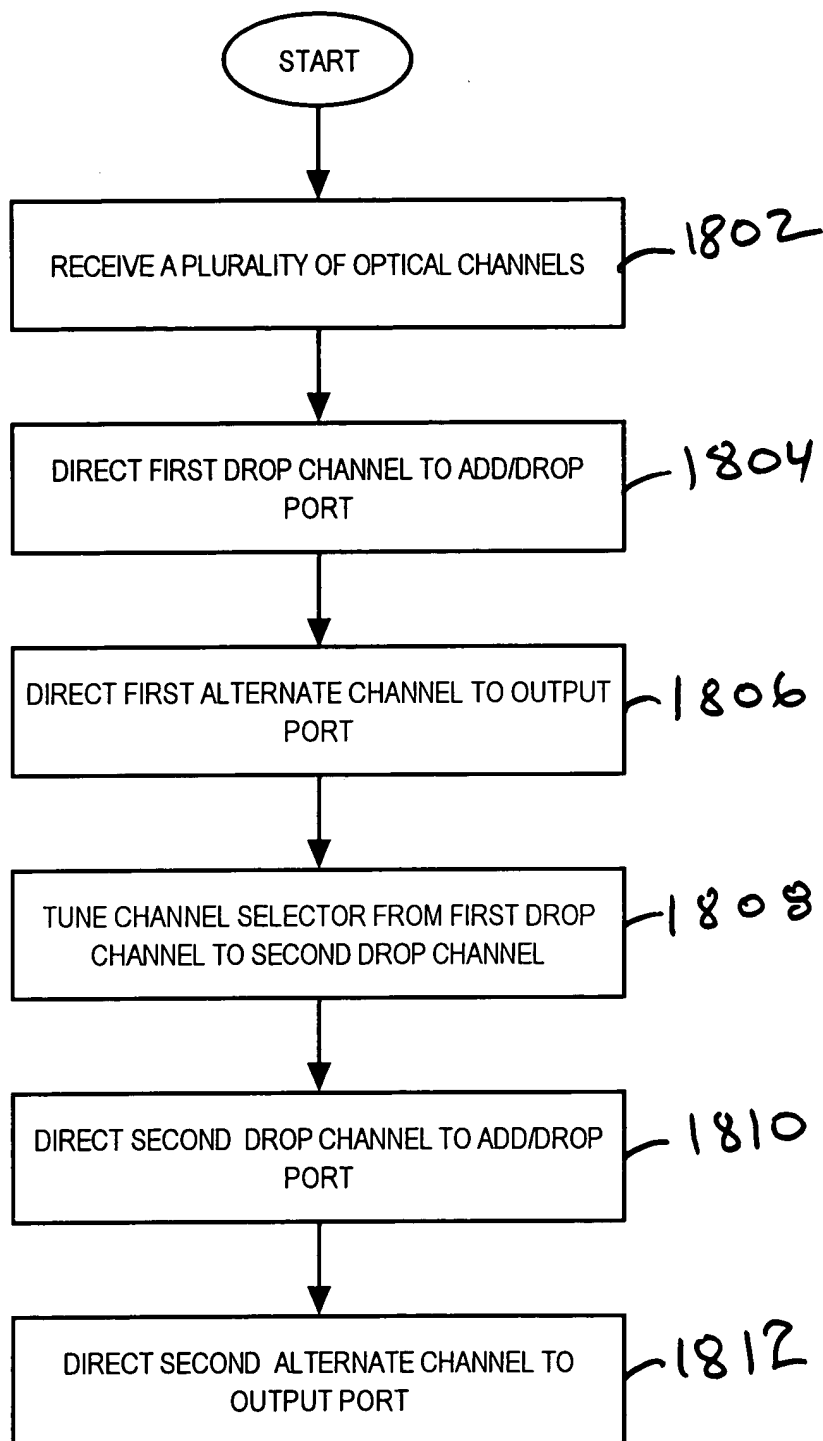


FIG. 18

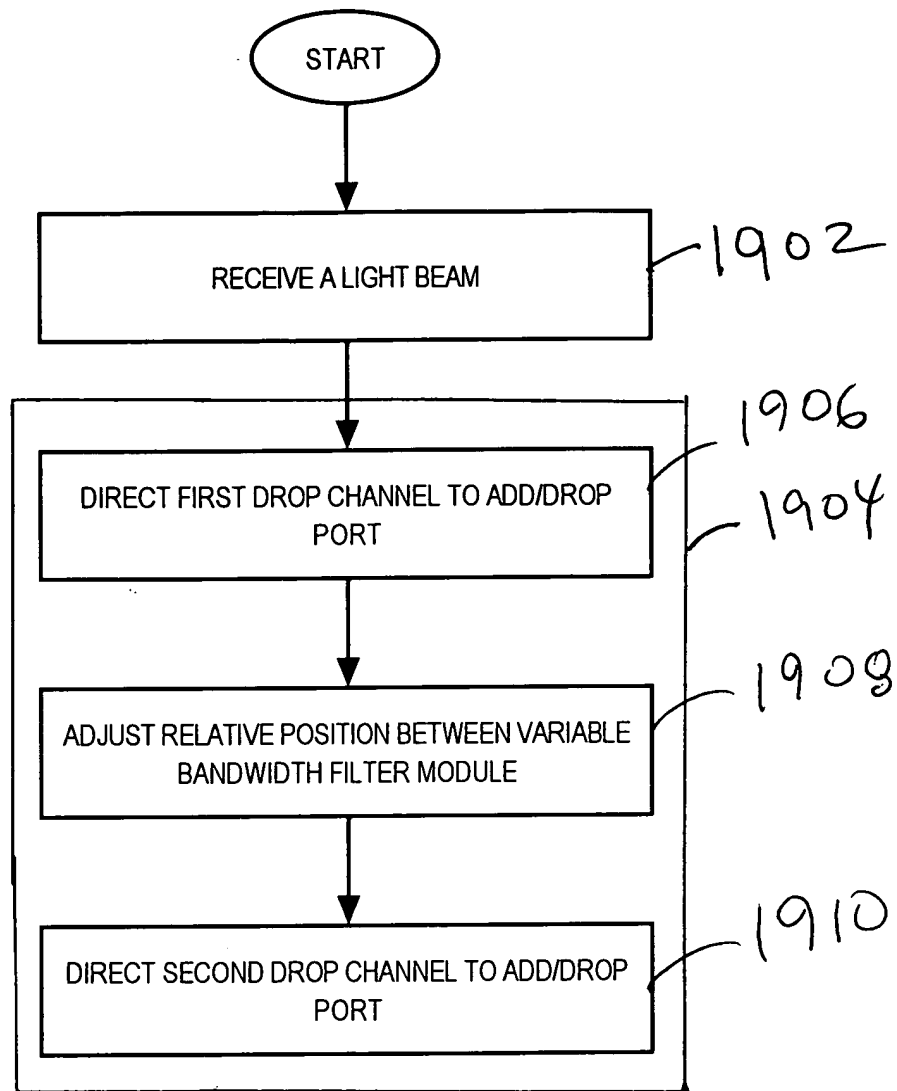


FIG. 19